

LSAIMO4 Al module

LS communication Thermocouple inputs: 7 -20~80mV

■Summary



*Number of inputs : 7 /Thermocouple input

(+1 /Cold junction compensation input,

Channel individual isolation)

*Input range : -20~80mV

*Module ambient temperature -5 to 60°C

*Insulation method : Photocoupler insulation



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■ Specifications

| | ITEM | | SPECIFICATION |
|--------------------------------------|----------------------------|--------------|---|
| Input | Number of channels | | 7 points +1 point cold junction compensation (channel individual isolation) |
| | Resolution | | 16bits |
| | Burn Up/Down | | Switchable by EMS setting |
| | Range | | -20 to 80mV (Full Scale) |
| | | TC | T: -200 to 400°C , -5.603 to 20.872mV |
| | | | J: −200 to 1200°C , −7.890 to 69.553mV |
| | | | E: -200 to 1000°C , -8.825 to 76.373mV |
| | | | R: -50 to 1768°C , -0.226 to 21.101mV |
| | | | K: -200 to 1372°C , -5.891 to 54.886mV |
| | | RTD | PT100: -10 to 100°C , 96.09 to 138.51Ω (Full Scale) |
| Absolute accuracy @25°C | TC | Type-T,J,K,E | ± 1°C |
| | | Type-R | ± 4°C (<0°C), ± 3°C (0°C~200°C), ± 2°C (>200°C) |
| | RTD | Type-PT100 | ± 0.1°C |
| Temperature drift | | | Less than ±100ppm/°C (relative to full-scale) |
| @5℃~60℃ | | | Less than ±100ppm/°C (relative to full-scale) |
| CMRR | TC | Туре-Е | 100dB or more attenuation |
| | | Type-T | 100dB or more attenuation |
| | RTD | When voltage | 100dB or more attenuation |
| | | When Current | 100dB or more attenuation |
| NMRR | TC | | 20dB or more attenuation |
| | RTD | | 20dB or more attenuation |
| Data refresh cycle | | | 50ms / All channels |
| Input filter | | | Software digital filter (Channel individual) |
| Dielectric strength | | | AC500V input terminal - between PE |
| | | | Between input channels |
| Communication with IOA | Communicaton method | | LVDS |
| | Communication speed | | 100Mbps |
| Self-diagnostic functions | | | Power voltage check (24V, 3.3V, 1.2V) |
| Detective | | | Clock check (FPGA-MCU for diagnosis, MCU for diagnosis -FPGA) |
| | | | Heartbeat check (FPGA-MCU for diagnosis, MCU for diagnosis -FPGA) |
| | | | CRC check (FPGA) |
| | | | Al communication error check ADC abnormal check |
| Detective | | | ADC aprormal check I/O signal range check (Overrange , Underrange) |
| Operation at disconnection | | | 1 to 7ch(For Burn Up setting) Detect overrange |
| operation at disconnection | | | 1 to 7ch(For Burn Down setting) Detect overlange |
| | | | 8ch Simultaneous detection of underrange and overrange |
| Protection (Power supply protection) | | ection) | Overvoltage protection |
| | (, | , , | Overcurrent protection |
| Indicator Display LED | | | 4 : RUN(Run) / STS(Status) /NSA(Network status A) / NSB(Network status B) |
| Insulation method | | | Photocoupler insulation |
| Hot swap | | | Possible |
| Power supply | | | DC24V ±20% (The voltage supplied from the backplane) |
| Environmental conditions | Module ambient temperature | | (Operating) -5 to 60°C |
| | | | (Storage) -40 to 85°C |
| | Module ambient humidity | | Less than 95%RH (No condensation) |
| Vibration | | | 3.5mm @5 to 8.4Hz |
| | | | 1G @8.4 to 150Hz |
| Shock | | | 15G 11ms |
| Current consumption | | | Less than 68mA |
| Weight | | | 0.10kg |
| Dimensions | | | 62mmD x 94mmH x 46mmW (Except projection) |
| Standard/Directive | | | IEC61131-2: 2007, RoHS |

^{*}About compliant module type

For compliant modules of this product, please refer to "Compliant backplane list (CGS-S9901-E-XX)".

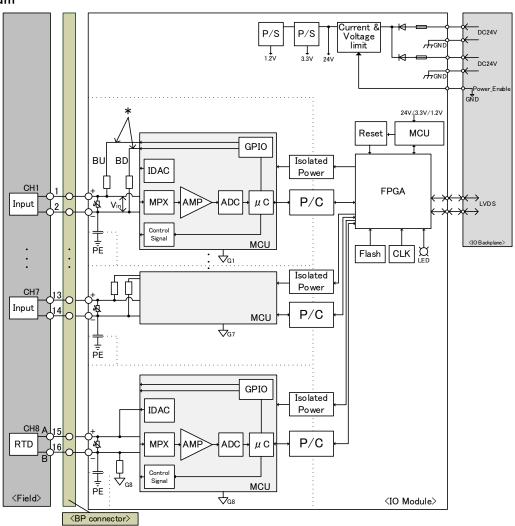
For compliant modules of this product, please refer to "Compliant accessory connector list (CGS-S9902-E-XX)".



Al module LSAIM04

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■Block diagram



The setting of burn up / burn down is determined by which side of + side / - side is made High.

- Normal time: The electromotive force of the compensation conductor is input to the MCU at Vin (+/- line voltage)
- ·At the time of disconnection (burn-up setting): the potential of the + side wiring rises ⇒ The value of Vin (the difference between the + side and the - side) becomes too large. (The ADC input sticks to the upper limit) ⇒ Overrange
- -At the time of disconnection (burndown setting): the potential of the side wiring rises \Rightarrow The value of Vin (the difference between the + side and the - side) becomes too small. (The ADC input sticks to the lower limit) ⇒ Underrange

P/S Power supply

IDAC lout Digital analog converter

MPX Multiplexer **AMP Amplifier**

ADC Analog digital converter

μC Micro controller

ČLK Clock

FPGA Field programmable gate array

LED Light emitting diode MCU Micro control unit

GND,G1 Ground IOA I/O adapter

LVDS Low Voltage Differential Signaling

ΒP Backplane Protective Earth PE Photocoupler Varistor Resistor Fuse Diode Capacitor

When using, please read the instruction manual attached to the product carefully and use it properly.

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