

LSAOMO1-1 AO module

LS communication Analog outputs: 8 4~20mA/0~20mA

■Summary



: 8 (Channel individual isolation) *Number of outputs

*Output range : 4 to 20mA/0 to 20mA(Selectable)

*Resolution : 16bits

*Module ambient temperature : -5 to 60°C

* Isolation : Trans isolation



LSAOM01-1 **AO** module

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■ Specifications

ITEM		SPECIFICATION
	Number of channels	8 (Channel individual isolation)
Output	Range	4 to 20mA/0 to 20mA (Switchable by EMS setting)(Full Scale)
	Resolution	16bits
	Minimum external impedance	24.9 Ω
	Maximum external impedance	750 Ω
	Switching time of the	2ms
	redundant configuration	
Data refresh cycle		5ms / All channels
Operation cycle usable in DPS		10 msec or more
Absolute accuracy	@25°C	±0.1%F.S.
Temperature drift	@5 to 60°C	Less than ±100ppm/°C (relative to full-scale)
Input filter		Software digital filter (Channel individual)
Dielectric strength		AC500V input terminal – between PE
		Between input channels
Communication with IOA	Communication method	LVDS
	Communication speed	100Mbps
HART communication compliant	Communication method	HART communication (superimposed on 4 to 20mA signal)
Between actuators		
Communication specification	Communication speed	1200bps
		Power voltage check (24V, 3.3V, 1.2V)
Self-diagnostic functions		Clock check (FPGA-MCU for diagnosis, MCU for diagnosis -FPGA)
		Read-back error check
		CRC check (FPGA)
		Tuning check
Detective		ADC abnormal check
		I/O signal range check (Range over, Range under)
		Disconnection detection (Settable of possible/none by the configuration)
Protection	(Power supply protection)	Overvoltage protection
L.P.	D: 1 1 ED	Overcurrent protection
Indicator	Display LED	4: RUN(Run) / STS(Status) /NSA(Network status A) / NSB(Network status B)
Insulation method		Transformer insulation
Hot swap		Possible
Power supply		DC24V ±20% (The voltage supplied from the backplane)
Environmental conditions	Module ambient temperature	(Operating) -5 to 60°C (Storage) -40 to 85°C
	Madula ambient humidity	Less than 95%RH (No condensation)
Module ambient humidity		3.5mm @5 to 8.4Hz
Vibration		
		1G @8.4 to 150Hz
Shock		15G 11ms
Current consumption		Less than 288mA
Weight		0.11kg
Dimensions Observed and / Discontinuous		62mmD x 94mmH x 46mmW (Except projection)
Standard/Directive		IEC61131-2 : 2007, RoHS

*About compliant module type

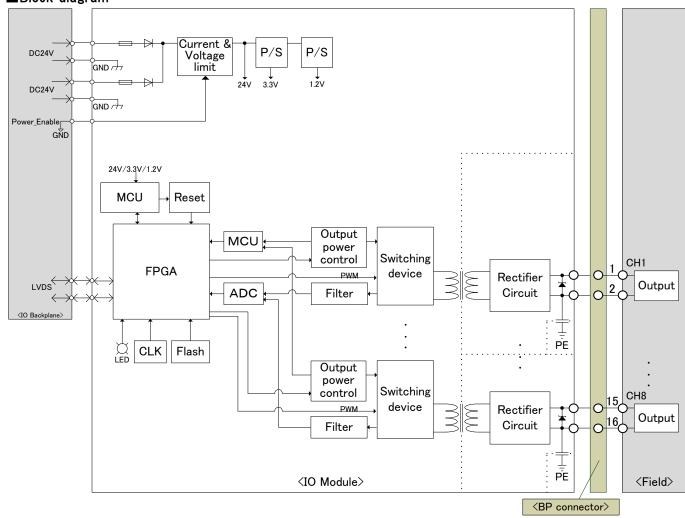
For compliant modules of this product, please refer to "Compliant backplane list (CGS-S9901-E-XX)".

For compliant modules of this product, please refer to "Compliant accessory connector list (CGS-S9902-E-XX)".

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■Block diagram



P/S : Power supply

PWM : Pulse width modulation
ADC : Analog digital converter

CLK : Clock

FPGA : Field programmable gate array

LED : Light emitting diode MCU : Micro control unit

GND : Ground IOA : I/O adapter

LVDS : Low Voltage Differential Signaling

When using, please read the instruction manual attached to the product carefully and use it properly.

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