

LSDOM01-1-R DO module

LS communication Digital outputs : 8

■ Summary



- * Number of outputs : 8 (Channel individual isolation)
- * Contact : Dry / A contact (Normally Open)
- * Contact method : Open drain
- * Module ambient temperature : 0 to 55°C
- * Insulation method : Transformer insulation
- * Supported FXtoLS adapter : LSRLTS-AI01, LSRLTS-DO01



This module is dedicated to compact retrofit terminal blocks.
Dedicated lock pins are attached to the bottom.

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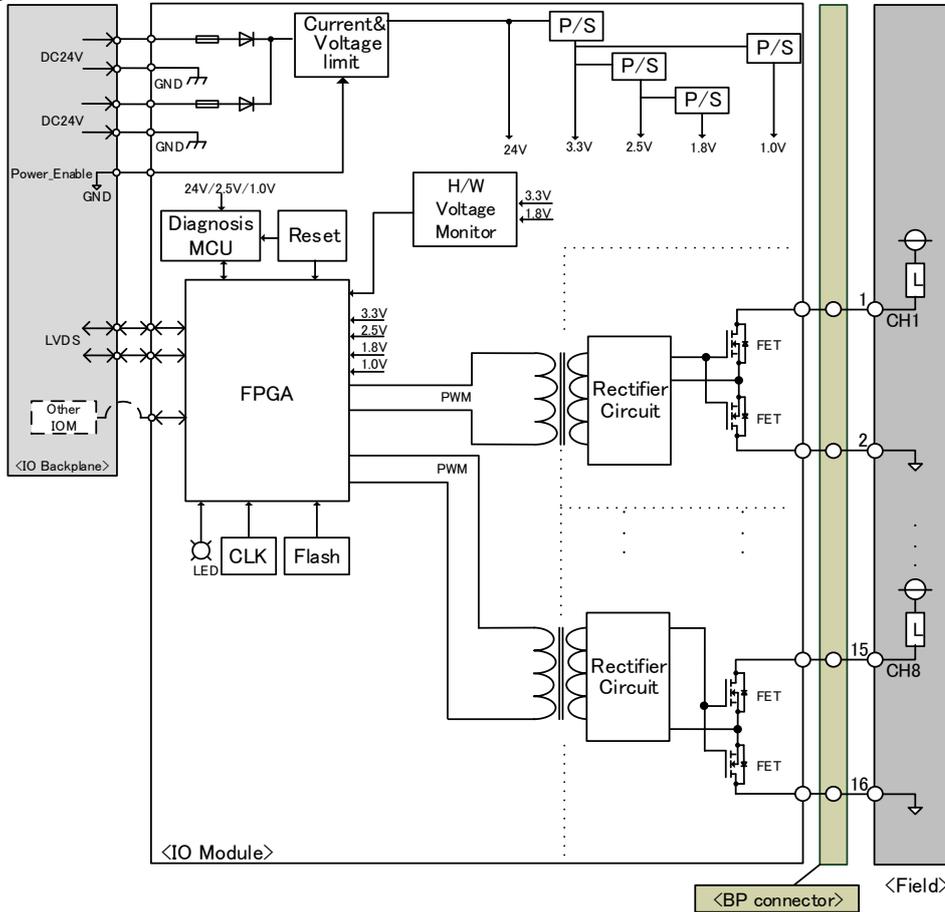
■ Specifications

ITEM		SPECIFICATION
Output	Number of channels	8 (Channel individual isolation)
	Contact	Dry / A contact (Normally Open)
	Contact on resistance	3 Ω or less
	Max rating contact voltage	DC 300 V/AC 264 V
	Max contact current	Continuous 0.3 A
	Data refresh cycle	ON → OFF : 1 ms or less OFF → ON : 1 ms or less
Operation cycle usable in DPS		5 msec or more
Dielectric strength		AC 1500 V Between output terminal and PE Between output channels
Communication with IOA	Communication method	LVDS
	Communication speed	100 Mbps
Self-diagnostic functions		Power voltage check (24 V, 3.3 V, 2.5 V, 1.8 V, 1.0 V) Clock abnormal check (FPGA-MCU for diagnosis, MCU for diagnosis -FPGA) Heartbeat check (FPGA-MCU for diagnosis, MCU for diagnosis -FPGA) CRC check (FPGA)
Protection	(Power supply Protection)	Overvoltage protection Overcurrent protection
Indicator	Display LED	4: RUN(Run) / STS(Status) / NSA(Network status A) / NSB(Network status B)
	Channel status LED	8: Ch 1 to Ch 8
Insulation method		Transformer insulation
Hot swap		Possible
Power supply		DC 24 V ±20% (The voltage supplied from the backplane)
Environmental conditions	Module ambient temperature	(Operating) 0 to 55°C (Storage) -40 to 85°C
	Module ambient humidity	(Operating / Storage) 10 to 95% RH (No condensation)
Vibration		3.5 mm @5 to 8.4 Hz 1 G @8.4 to 150 Hz
Shock		15 G 11 ms
Rated Current		75 mA
Weight		0.10 kg
Dimensions		62 mm (D) x 94 mm (H) x 46 mm (W) (Except projection)
Standard/Directive		EN 61131-2:2007, RoHS

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■ Block diagram



P/S	:	Power supply
PWM	:	Pulse width modulation
FET	:	Field effect transistor
CLK	:	Clock
FPGA	:	Field programmable gate array
LED	:	Light emitting diode
MCU	:	Micro control unit
GND	:	Ground
IOM	:	I/O module
IOA	:	I/O adapter
LVDS	:	Low Voltage Differential Signaling
BP	:	Backplane
	:	Fuse
	:	Diode
	:	Power supply
	:	Load
	:	Transformer

When using, please read the instruction manual attached to the product carefully and use it properly.

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