

CPIOS01 SIOS card

CompactPCI / D-Ring interface card

■ Summary



*Function	:	PCI system controller D-Ring interface
*CPU	:	Renesas HD6417751RBG240V (SH4-7751R)
CPU clock	:	240MHz
CPU external bus clock	:	80MHz
*Boot ROM	:	512kB
*Main memory	:	DDR3-SDRAM DDR3-667 2Gbit_16bit width(with ECC)

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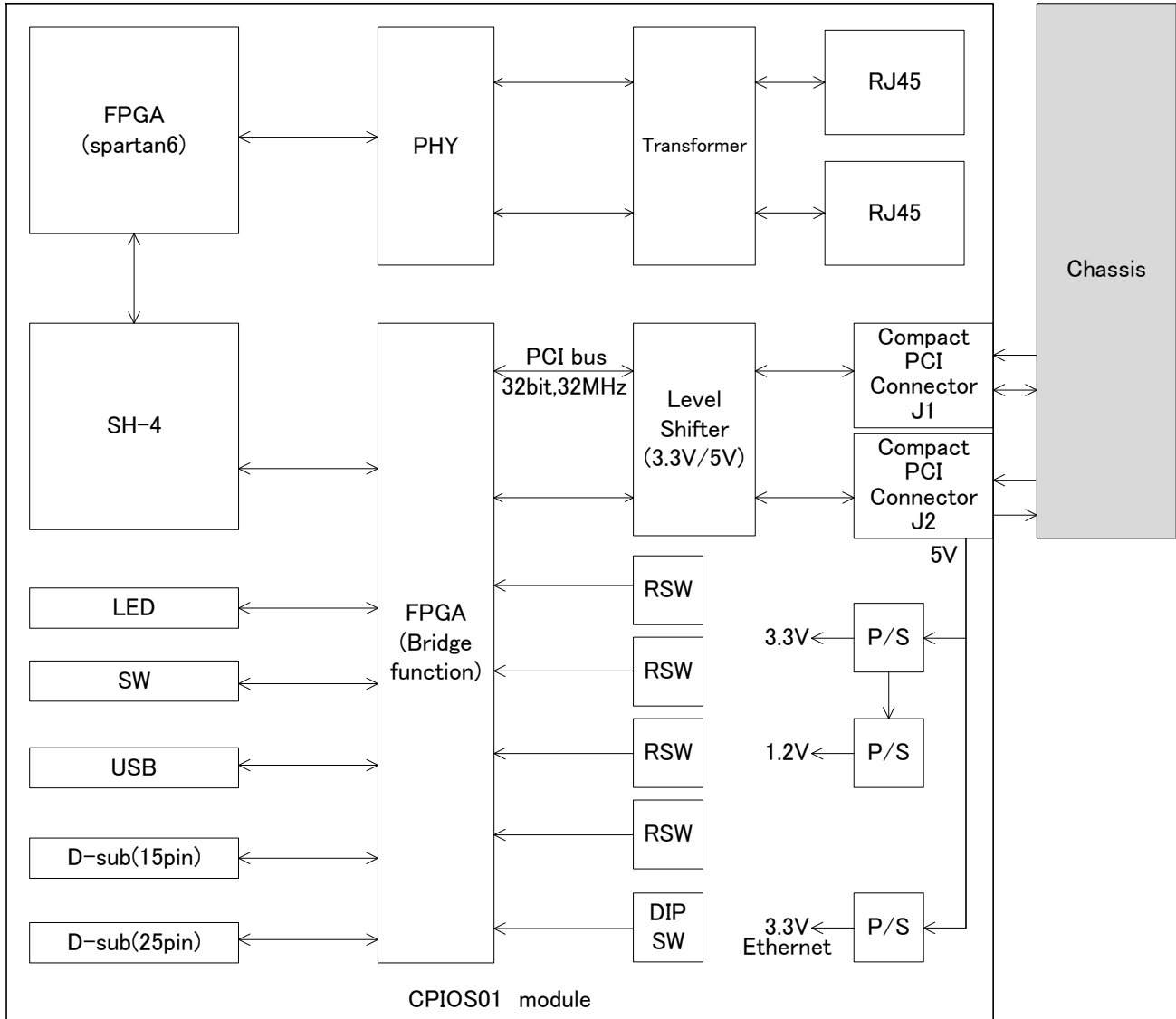
■ Specifications

ITEM		SPECIFICATION
Controller		Renesas Electronics SH4/240MHz Xilinx Spartan6 XC6SLX150
Communication rate		1000Mbps/1000BASE-T
Main memory1		DDR-667 use 2Gbit (DDR3-800 extensible with ECC)
Main memory2		DDR-667 use 2Gbit (DDR3-800 extensible with ECC)
External interface		Ethernet(1000Mbps) :2
		USB mini-B :1
		Push switch :3 (CONTROL,RESET,ABORT)
		Toggle switch with lock :1 (ON-LINE/SHUTDOWN)
		D-sub(25pin) :1 (Redundant status interface. P/C insulation)
		D-sub(15pin) :1 (CPU normal signal, Expansion DIO, EDI time adjust.) P/C insulation
On board switch		Rotary switch :4 (4bit_node No setting)
		Slide switch :3 (8bit, Priority of CPU single/Start control, Time server DI interrupt enable, PRSR# out enable, Clock error check, FPGA error check, CPSIO01/02 compatible mode)
Self-diagnostic functions		Power voltage check (3.3V, 1.8V, 1.5V, 1.23V) Clock abnormal check Heartbeat check CRC check(FPGA) ECC error check CPU clock stop check WDT check PCI bus abnormal check
Protection	(Power supply protection)	Overvoltage protection Overcurrent protection Power supply backup for real time clock, 110 hours.
Indicator	Display LED	4 Action mode(G : CONTROL Y : STANDBY R : INIT/OFF-LINE) Abnormal status(G : NORMAL Y : ABNORMAL R : FAILURE) PWR (Power) CF_ACT (Compact Flash Action)
Applicable chassis		CPCHS01 , CPCHS02, CPCHS11, CPCHS21,CPCHS24
Power supply		DC5V ±5% (The voltage supplied from the chassis)
Environmental conditions		Module ambient temperature (Operating) -5~60°C (Storage) -25~85°C
		Module ambient humidity 10~95%RH (No condensation)
Vibration		3.5mm @5Hz ~ 8.4Hz 1G @8.4Hz ~ 150Hz
Shock		15G 11ms
Current consumption		Less than 3.8A
Weight		0.40kg
Dimensions		174.4mmD x 128.7mmH x 60.6mmW (Except projection)
Directives		RoHS

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■ Block diagram



- RJ45 : Registered jack 45
- PHY : Physical layer chip
- FPGA : Field programmable gate array
- PCI : Peripheral component interconnect
- P/S : Power supply
- RSW : Rotary switch
- DIP SW : Dual In-line package switch
- LED : Light Emitting Diode
- SW : Switch
- USB : Universal serial bus
- D-sub : D-subminiature

When using, please read the instruction manual attached to the product carefully and use it properly.

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